

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

## LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

ATTORNEY DOCKET NO.  
1075.1174

APPLICATION NO.

FIRST NAMED INVENTOR  
Akio MATSUDA, et al.FILING DATE  
September 28, 2001

GROUP ART UNIT

JC997 U.S. PTO  
09/964591

09/26/01

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES NO
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						

## OTHER REFERENCES (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

<i>KM</i>	AM	K. V. Rompaey, et al., "COWARE - A DESIGN ENVIRONMENT FOR HETEROGENEOUS HARDWARE/SOFTWARE SYSTEMS", (Proceedings EuroDAC), 1996
<i>KM</i>	AN	S. Vernalde et al., "An Object Oriented Programming Approach for Hardware Design", (IEEE Computer Society Workshop on VLSI) 4/1999.
<i>KM</i>	AO	R. Cmar, et al., "A Methodology and Design Environment For DSP Asic Fixed Point Refinement", (Proceedings Design and Test in Europe Conference, pp. 271-276), 3/1999.

EXAMINER

*KM*

DATE CONSIDERED

2/1/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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<i>KJM</i>	AM	P. Schaumont, et al., "Hardware Reuse at the Behavioral Level", (The Proceedings of the 36 <sup>th</sup> Design Automation Conference, ACM, 1999, pp. 784-789) June 1999.
<i>KJM</i>	AN	S. Liao, et al., "An Efficient Implementation of Reactivity for Modeling Hardware in the Scenic Design Environment" Proceedings of the Design Automation Conference DAC'97, pp. 70-75, June 1999.
<i>KJM</i>	AO	A. Gerstlauer, et al., "SpecC SYSTEM – Level Design Methodology Applied to the Design of A GSM Vocoder", pp. 19-26, Proceedings of the Workshop on Synthesis and System Integration of Mixed Technologies) April 1999.

EXAMINER	<i>B. Schaefer</i>	DATE CONSIDERED	<i>2/1/05</i>
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<i>KM</i>	AM	D. Verkest et al., "On the use of C++ For System on Chip Design" (Proceedings of the IEEE Workshop on VLSI (IWV), pp. 42-47, April 1999.
<i>KM</i>	AN	A. Fukuda, "Parallel Operating System", pp. 30-32, Issued by Corona Publishing Co., Ltd.), 1997
<i>KM</i>	AO	S. Oaks, et al., "Java Thread Programming, pp. 1-6, Issued by Ohmsha, Ltd., 1997.

EXAMINER	<i>W. Hause</i>	DATE CONSIDERED	<i>2/1/05</i>
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